

CPU Operation Mode:

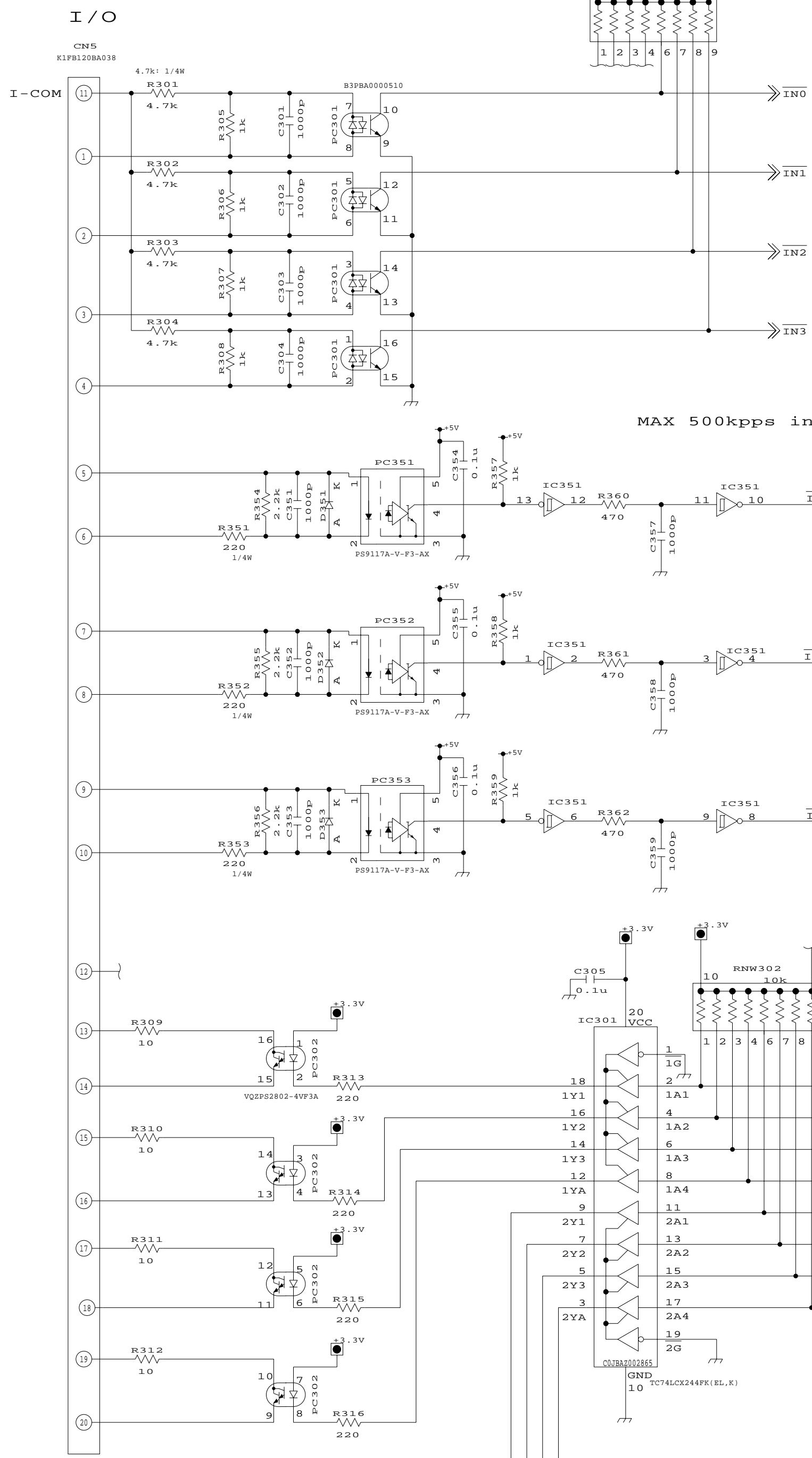
FWE	MD1	MD0	Description
0	1	0	Normal Operation
1	0	0	Boot Mode
1	1	1	USB Boot Mode

This schematic intends to use the boot mode for flash writing. If using the USB boot mode, change the pin configuration.

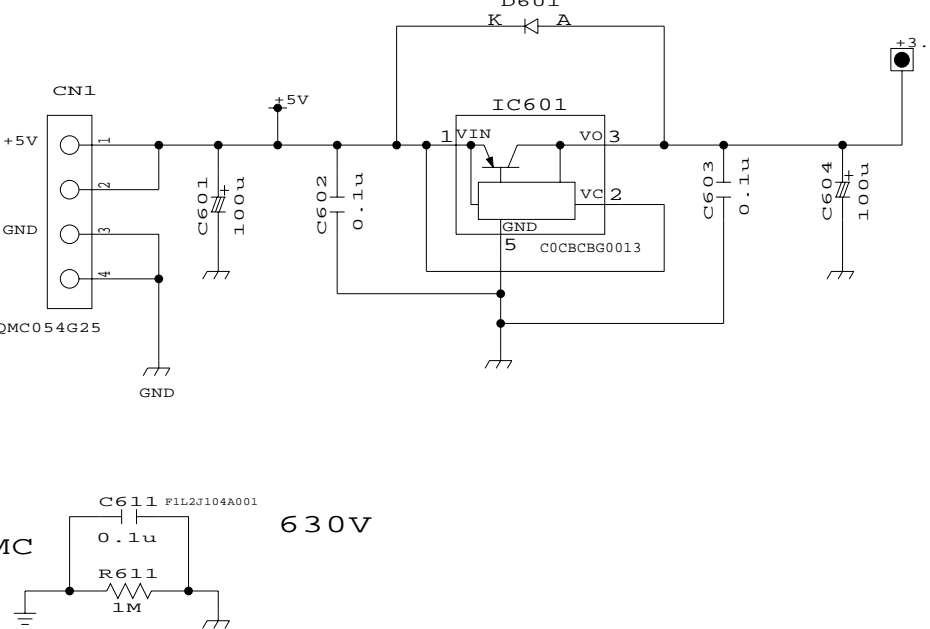
Rev. 2

REF No.	部 品 名 称	部 品 コード	開 連 REF.	数 量	単 位	TYPE
K1200	クリームハンダ	Z6030954	#RELAT	8	GM	4
K1100	ハンダ	Z6030133	#RELAT	3	GM	4
K1000	ハンダ	Z6030132	#RELAT	3	GM	4
PK100	プリント基板	581D744-	#RELAT	1	KO	8
Model: RTEX-NC-MODULE						
Name: RTEX MODULE BOARD						
Scale	Designed	Drawn	Checked	Checked	Sheet	
TAKAGI		TAKAKA	FUJISAKI	1/3		
No.: SZ-581D744						

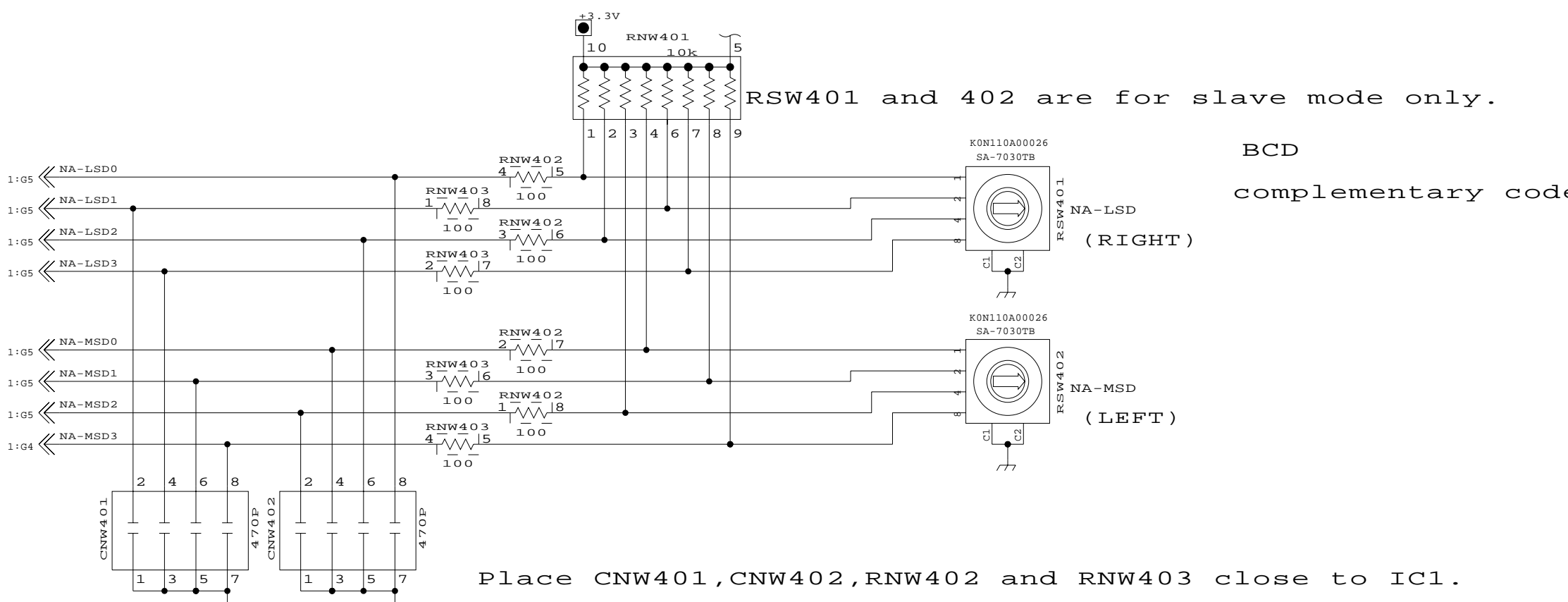
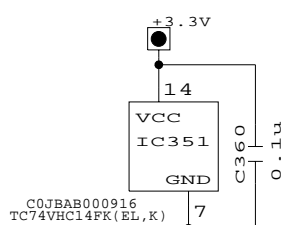
Tie the connector shell to Frame GND.



Power Supply



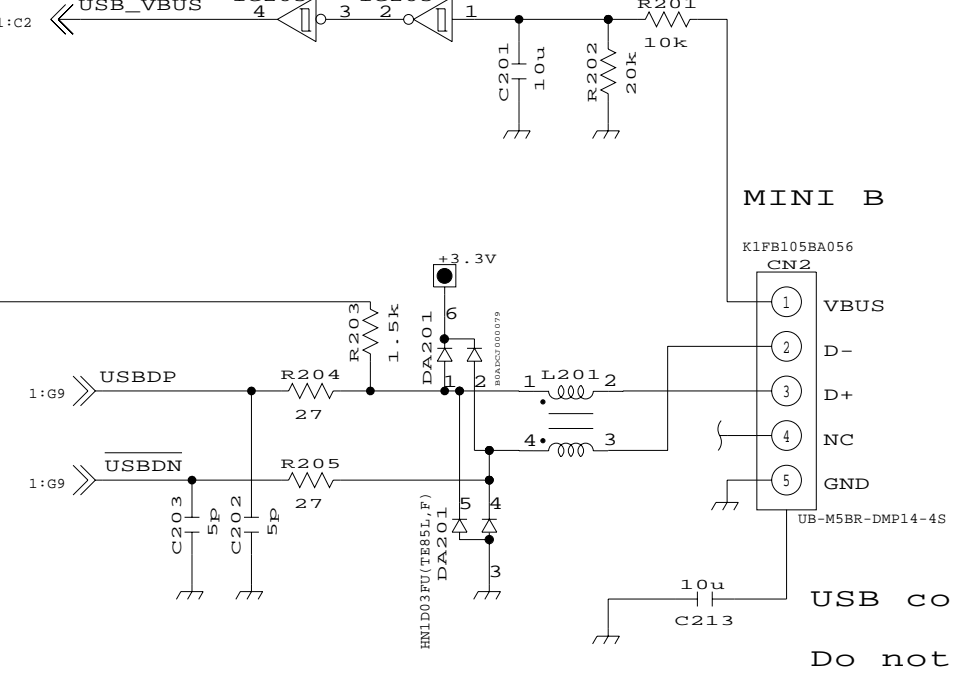
MAX 500kpps in quad edge evaluation



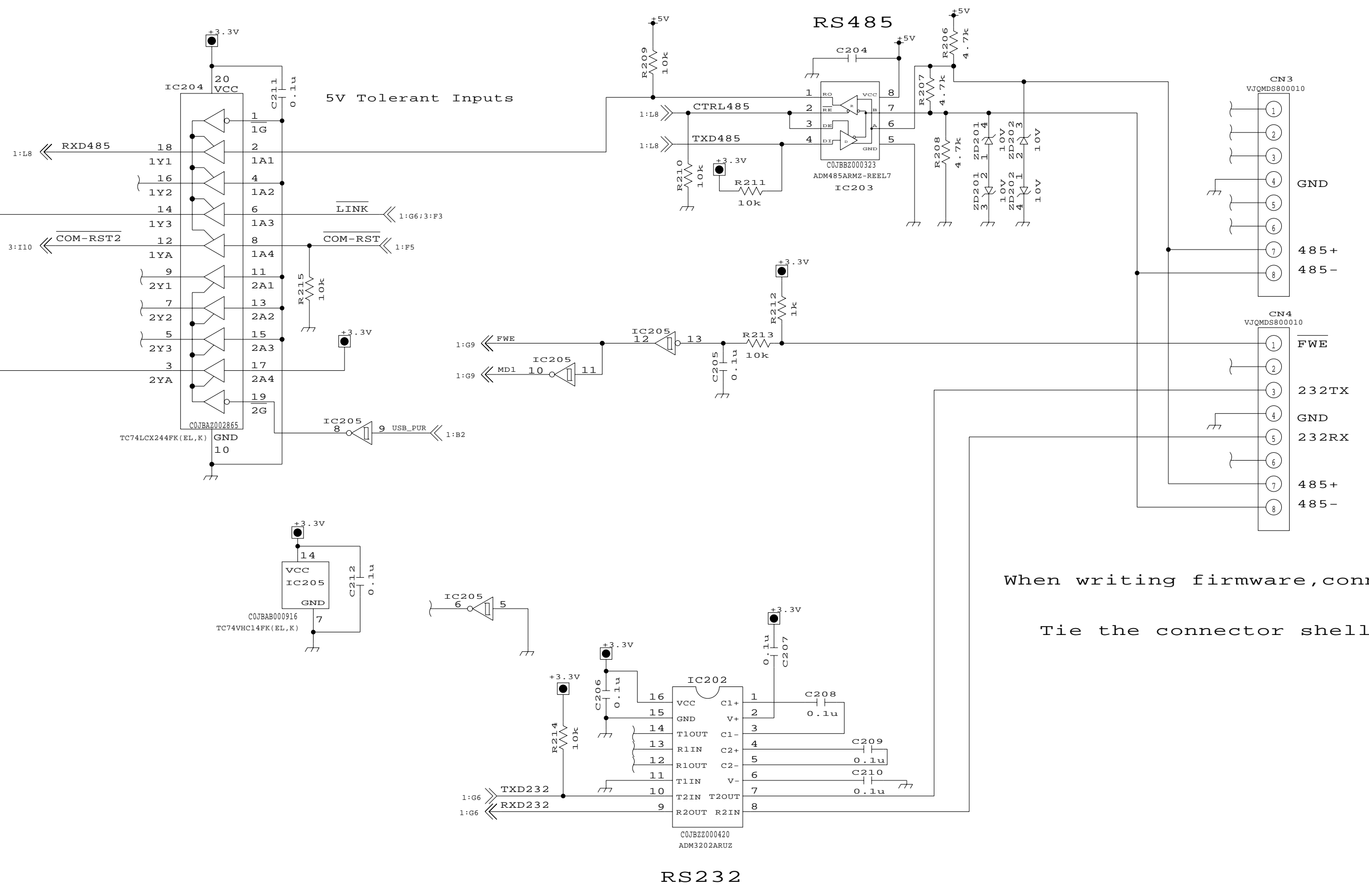
Place CNW401,CNW402,RNW402 and RNW403 close to IC1.

TO prevent ESD

USB



USB connector shell
Do not tie USB connector shell to Frame GND.



When writing firmware,connect FWE with GND.

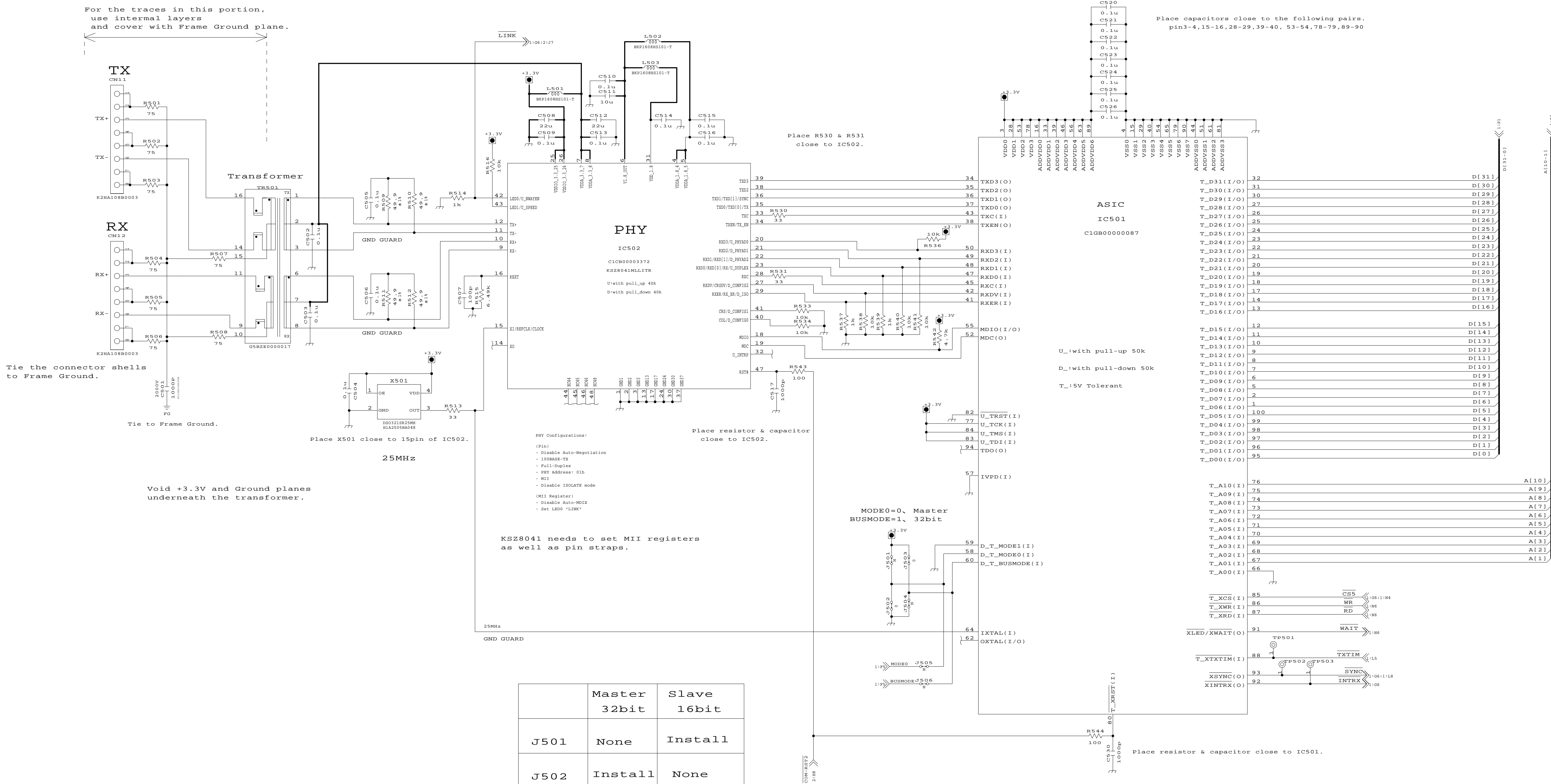
Tie the connector shells to Frame GND.

Rev. 2

						Model	RTEX-NC-MODULE	
Scale	Designed	Drawn	Checked	Approved	Sheet	Name	RTEX MODULE BOARD	
	TAKAGI		TAKAKURA	FUJISAKI	2/3	No.	SZ-581D744	
						2012/10/25		

For KSZ8041,use revision A5 or later.

Note: Regardless of actual link situation,pulldown R514 causes LINK active at reset.



	Master 32bit	Slave 16bit
J501	None	Install
J502	Install	None
J503	Install	None
J504	None	Install

						Model	RTEX-NC-MODULE
Scale	Designed	Drawn	Checked	Approved	Sheet	Name	RTEX MODULE BOARD
	TAKAGI		TAKAKO	FUJISAKI	3/3	No.	SZ-581D744
2013/10/25							