

SH7216 port assignment for PCB "581D744 Rev.2"

2012/10/25

"IN, OUT" shows direction as pin, and is not port setting. [] means to enable on-chip peripheral module, and in this case, the port should be set to input.

Rev. 1

											Blue cells are only for slave mode.				* Negative logic		
		15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
PAL	Function				COM-RST*	MNM1221 MODE0	MNM1221 BUSMODE	MNM1221 INTRX* [IRQ3]	MNM1221 SYNC* ([IRQ4])	MNM1221 CS* [CS5*]	LED ALM-G*	LED ALM-R*	RS232 TX [TXD1]	RS232 RX [RXD1]	LED COM-G*	LED COM-R*	PHY LINK*
		OUT	OUT	OUT	OUT	OUT	OUT	IN	IN	OUT	OUT	OUT	OUT	IN	OUT	OUT	IN
	PAIORL	1	1	1	1	1	1	0	0	0	1	1	0	0	1	1	0
	initial PADRL	0	0	0	0	0	1	0	0	0	1	1	0	0	1	1	0
pin#		16	17	18	97	98	99	100	101	102	103	162	161	160	159	158	157
PAH	Function	-	-	-	-	-	-	-	-	-	-	RD* [RD*]	WR* [WRL*]	MNM1221 WAIT* [WAIT*]			
												OUT	OUT	IN	OUT	OUT	OUT
	PAIORH	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1
	initial PADRH	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
pin#		-	-	-	-	-	-	-	-	-	-	9	10	11	12	14	15
PB	Function	USB PUPD	USB Boot_Clock _Sel	EEPROM SDA [SDA]	EEPROM SCL [SCL]	EEPROM WP				TEST7	TEST6	TEST5	TEST4	TEST3	TEST2	TEST1	TEST0
		OUT	IN	IN/OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT
	PBIORL	1	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	initial PBDRL	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0
pin#		117	116	111	110	55	54	53	52	48	47	46	45	44	43	42	41
PC	Function	IB* [TCLKD]	IA* [TCLKC]		MNM1221 TXTIM* [TCLKA]	MNM1221 TXTIM* [TIOC1B]	[A10]	[A9]	[A8]	[A7]	[A6]	[A5]	[A4]	[A3]	[A2]	[A1]	
		IN	IN	OUT	IN	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	IN
	PCIORL	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	initial PCDRL	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
pin#		37	36	35	34	33	32	31	30	28	27	26	25	24	23	22	21
PDL	Function	[D15]	[D14]	[D13]	[D12]	[D11]	[D10]	[D9]	[D8]	[D7]	[D6]	[D5]	[D4]	[D3]	[D2]	[D1]	[D0]
		IN/OUT	IN/OUT	IN/OUT	IN/OUT	IN/OUT	IN/OUT	IN/OUT	IN/OUT	IN/OUT	IN/OUT	IN/OUT	IN/OUT	IN/OUT	IN/OUT	IN/OUT	IN/OUT
pin#		74	73	72	71	70	69	68	67	64	63	62	61	60	59	58	57
PDH	Function	[D31]	[D30]	[D29]	[D28]	[D27]	[D26]	[D25]	[D24]	[D23]	[D22]	[D21]	[D20]	[D19]	[D18]	[D17]	[D16]
		IN/OUT	IN/OUT	IN/OUT	IN/OUT	IN/OUT	IN/OUT	IN/OUT	IN/OUT	IN/OUT	IN/OUT	IN/OUT	IN/OUT	IN/OUT	IN/OUT	IN/OUT	IN/OUT
pin#		94	93	92	91	90	89	88	87	84	83	82	81	80	79	78	77
PE	Function	OUT3*	OUT2*	OUT1*	OUT0*	IN3*	IN2*	IN1*	IN0*	IZ* [TIOC2B]	RS485 RX [RXD3]	RS485 TX [TXD3]	RS485 CTRL				MNM1221 SYNC* [TIOC0A]
		OUT	OUT	OUT	OUT	IN	IN	IN	IN	IN	IN	OUT	OUT	OUT	OUT	OUT	IN
	PEIORL	1	1	1	1	0	0	0	0	0	0	0	1	1	1	1	0
	initial PEDRL	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
pin#		173	172	171	170	169	167	168	166	165	6	5	4	3	2	1	176
PF	Function	-	-	-	-	-	-	-	-	RSW NA-MSD3	RSW NA-MSD2	RSW NA-MSD1	RSW NA-MSD0	RSW NA-LSD3	RSW NA-LSD2	RSW NA-LSD1	RSW NA-LSD0
										IN	IN	IN	IN	IN	IN	IN	IN
pin#		-	-	-	-	-	-	-	-	149	148	147	146	141	140	139	138